

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): James S. McCormick, et al.

Title: **MULTIPROCESSOR CONTROL BLOCK FOR USE IN A
COMMUNICATION SWITCH AND METHOD THEREFORE**

App. No.: 09/746,602

Filed: 12-21-2000

Examiner: Vanderpuye, Kenneth N. Group Art Unit: 2661

Atty. Dkt. No. 1400.4100220

Mail Stop Amendment
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

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RESPONSE

Dear Sir:

In response to the Office action of March 4, 2004, Applicant submits the following:

REMARKS/ARGUMENTS

Claims 1-31 are pending in the present application. The Examiner has allowed claims 1-23. The Examiner has rejected claim 24. The Examiner has objected to claims 25-31. Applicant respectfully requests reconsideration of pending claims 24-31.

The Examiner has rejected claim 24 under 35 U.S.C. § 102(b) as being anticipated by Osaki (U.S. Patent No. 5,657,449). Applicant respectfully disagrees. Applicant submits that Osaki fails to disclose the present invention as set forth in claim 24. For example, Applicant submits that Osaki fails to disclose "receiving a first ingress data unit corresponding to a call." Applicant notes that, in col. 6, lines 63-67, Osaki appears to distinguish the operations of line setter 211 and call setter 212, stating, "...the line setter 211 performs the operation associated with the setting of a line channel, while the call setter 212 performs the operation associated with the management of user's communication mode..." On the contrary, the Examiner appears to allege that the "line setting signal" (col. 6, lines 34-35) of Osaki teaches "a first ingress data unit corresponding to a call," but does not attempt to